

# **Parallelism Analysis And Extraction Of Digital Signal Processingalgorithms**

Elleithy, K.M. Amin, A.A.M.;Dept. of Comput. Eng., King Fahd Univ. of Pet.Miner.,  
Dhahran;

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King Fahd University of Petroleum & Minerals

**<http://www.kfupm.edu.sa>**

## **Summary**

A new approach for parallelism analysis and extraction of digital signal processing algorithms is introduced. The high level description of the input is given in CIRCAL. A dependency graph of the problem is constructed to check existence of cycles. Loops in the dependency graph are parallelized. The approach is illustrated by an example

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